

TRANSLATION of related part of Form PCT/ISA/237

PATENT COOPERATION TREATY

From Japanese Patent Office
(INTERNATIONAL SEARCH AUTHORITY)

To: HAYASE, Kenichi HAYASE & CO. 13F, NISSAY SHIN-OSAKA Bldg., 3-4-30, Miyahara, Yodogawa-ku, Osaka-shi, Osaka 532-0003 JAPAN	PCT WRITTEN OPINION OF THE ISA (PCT Rule 43bis)
	Date of Mailing 07 December 2004

Applicant's or agent's file reference P32927-P0		See item 2 below for the subsequent procedure	
International application No. PCT/JP2004/012246	International filing date 19 August 2004	Priority date 29 August 2003	
International Patent Classification (IPC) or national classification and IPC Int. Cl⁷ H03M 13/15			
Applicant Matsushita Electric Industrial Co., Ltd.			

1. This opinion contains indications relating to the following items:

- I Basis of the opinion
- II Priority
- III Non-establishment of report with regard to novelty, inventive step or industrial applicability
- IV Lack of unity of invention
- V Reasoned statement under Rule 43,2.1(a)(i) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement
- VI Certain documents cited
- VII Certain defects in the international application
- VIII Certain observations on the international application

OMISSION(2 and 3)

Date of completion of this opinion 19 November 2004

Name and mailing address of the ISA/JP Japanese Patent Office	Authorized officer Telephone No.
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TRANSLATION of related part of Form PCT/ISA/237

WRITTEN OPINION OF THE ISA

International application No.
PCT/JP2004/012246

I . Basis of the opinion

1. This opinion has been drawn on the basis of the language of international application, unless otherwise indicated below.

OMISSION(2, 3, and 4)

TRANSLATION of related part of Form PCT/ISA/237

WRITTEN OPINION OF THE ISA

International application No.
PCT/JP2004/012246

V Reasoned statement under Rule 43.2.1(a)(i) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. STATEMENT

Novelty (N)	Claims	1-14	YES
	Claims	NONE	NO
Inventive Step (IS)	Claims	NONE	YES
	Claims	1-14	NO
Industrial Applicability (IA)	Claims	1-14	YES
	Claims	NONE	NO

2. CITATIONS AND EXPLANATIONS

Reference 1: JP 2001-23316 A (Hitachi, Ltd.) 2001.01.26, whole text,
whole figure

Reference 2: JP 2001-292066 A (Sanyo Electric Co., Ltd.)
2001.10.19, paragraphs [0001]-[0035], figures 17-25
& US 2001/0014960 A1 & CA 1318836 A

Claims 1-14

Reference 1 cited in the International Search Report discloses an error detection apparatus which performs error correction and error detection simultaneously on a target code string comprising plural sectors, each sector comprising matrix data, which apparatus includes a means for updating the result of error detection code operation on the basis of the result of error correction. Further, it is also described that scrambles are removed on the basis of the updated result of error detection code operation.

Reference 2 cited in the International Search Report (particularly refer to description relating to [Fig.23]) discloses a means for performing offset calculation (corresponding to "skip operation") when a target code string comprising plural sectors, each sector comprising matrix data, is inputted in discontinuous data arrangement (PO direction).

It is recognized that performing the offset calculation described in Reference 2 when inputting data in the PO direction to the error detection apparatus described in Reference 1 is easily conceived by those skilled in the art.

In Reference 1, it is recognized that storing the result of operation in a memory is arbitrarily carried out.